App No.: NOT YET ASSIGNED Docket No.: M4065.0628/P628-B Inventor: Eric R. Fossum Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF FORMATION 36 V<sub>out</sub> <sup>20</sup> 32  $V_{dd}$ ~15 30<sup>J</sup> -20-16 FIG. 1 136 133 133 138a 136a 134ر <u>150</u> <del>|</del> 120 -110 FIG. 2 138 138a 136a <u>150</u>

136a 138a 138 150 124 P 126 N 120 188

<u>150</u>

FIG. 3

Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET

CHANNEL AND METHOD OF FORMATION

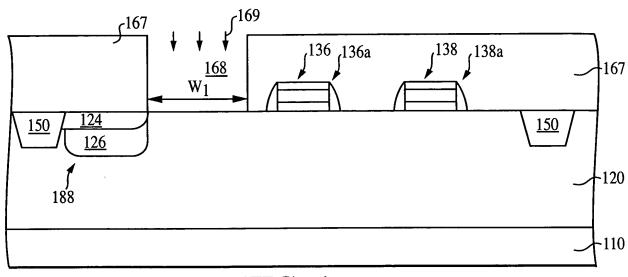


FIG. 4

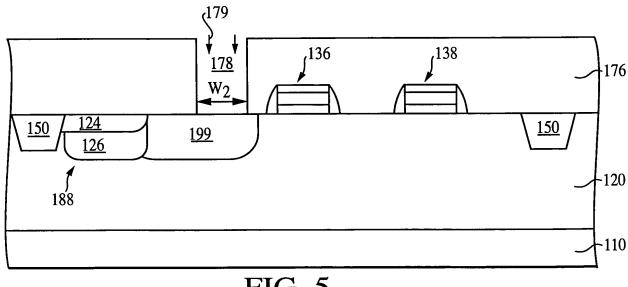


FIG. 5

App No.: NOT YET ASSIGNED Docket No.: M4065.0628/P628-B Inventor: Eric R. Fossum Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF FORMATION



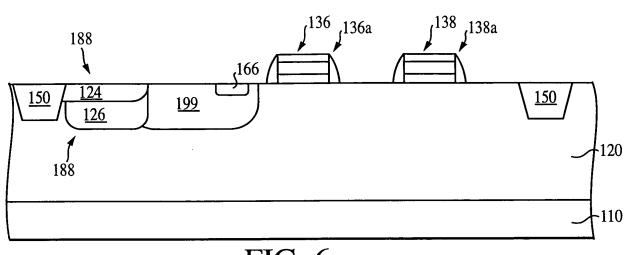
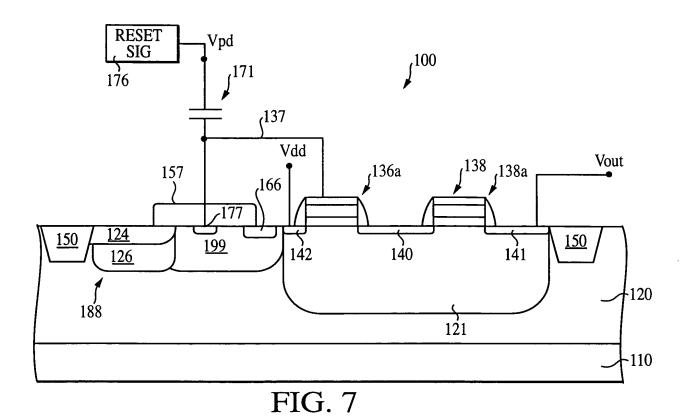


FIG. 6

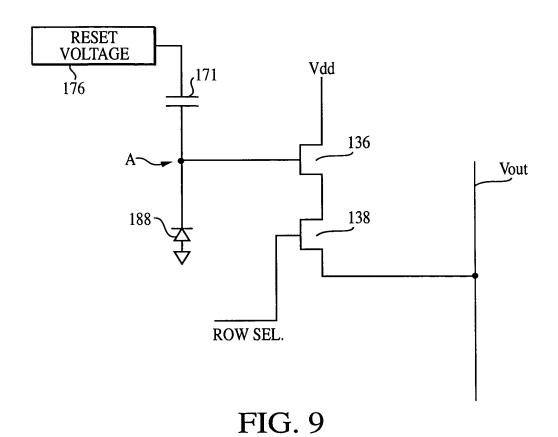
Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF FORMATION



**7** 147 138

FIG. 8

Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF FORMATION



Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET

CHANNEL AND METHOD OF FORMATION

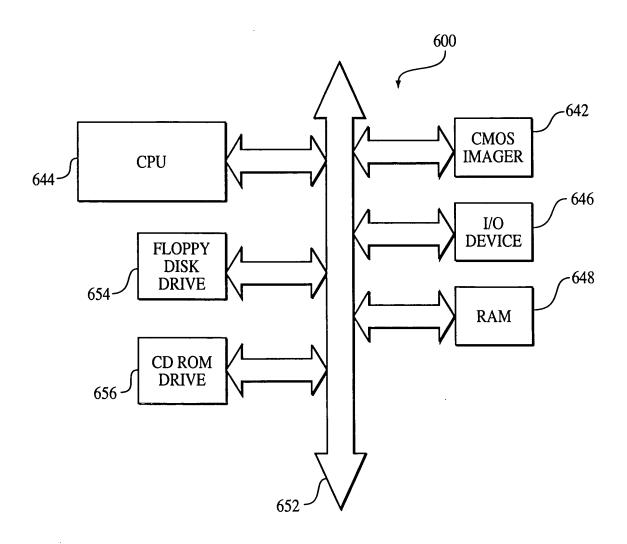


FIG. 10